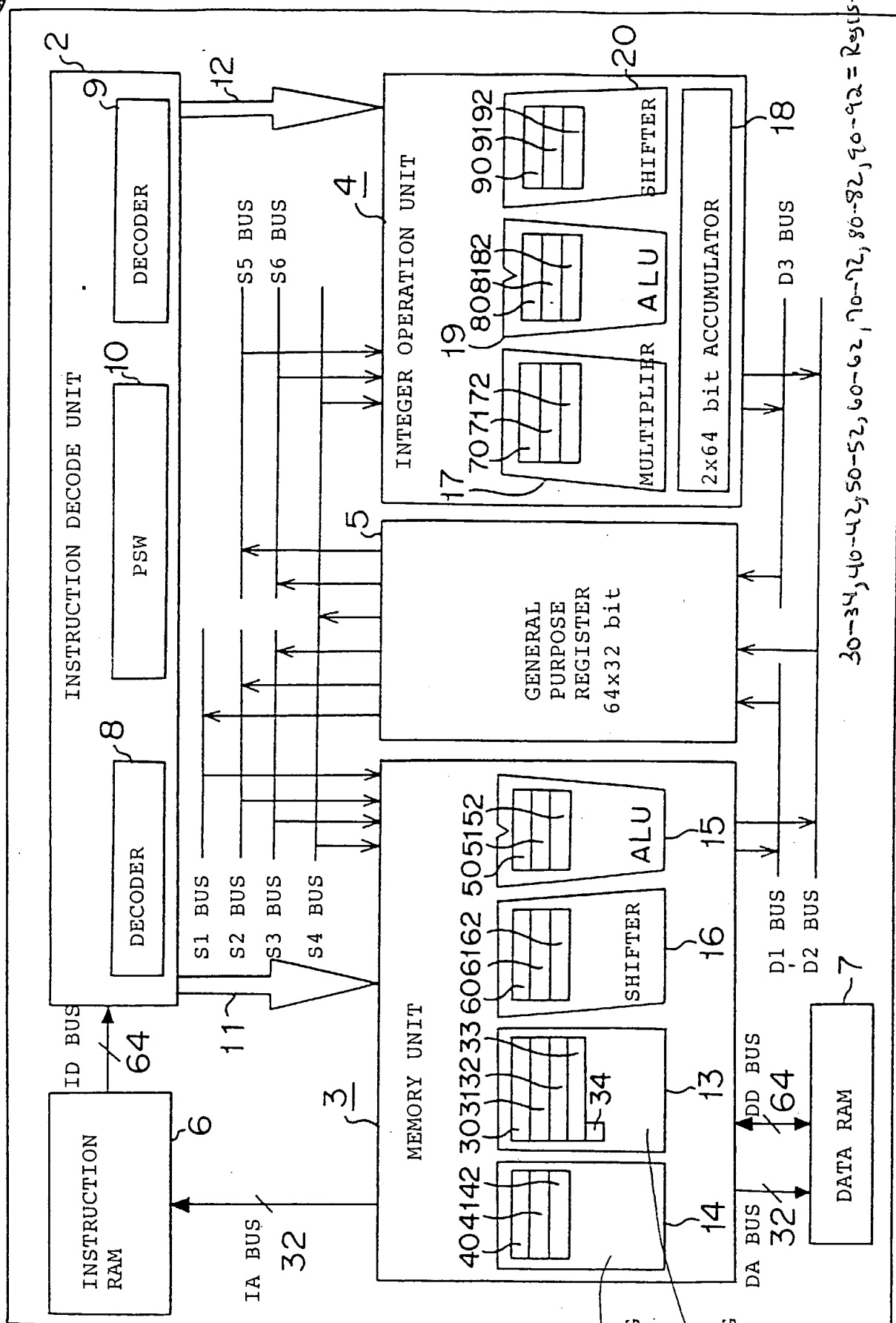


FIGURE 1



30-34, 40-42, 50-52, 60-62, 70-72, 80-82, 90-92 = Register

MEMORY CONTROLLING PART
 MEMORY CONTROLLING PART

FIGURE 2 (a)

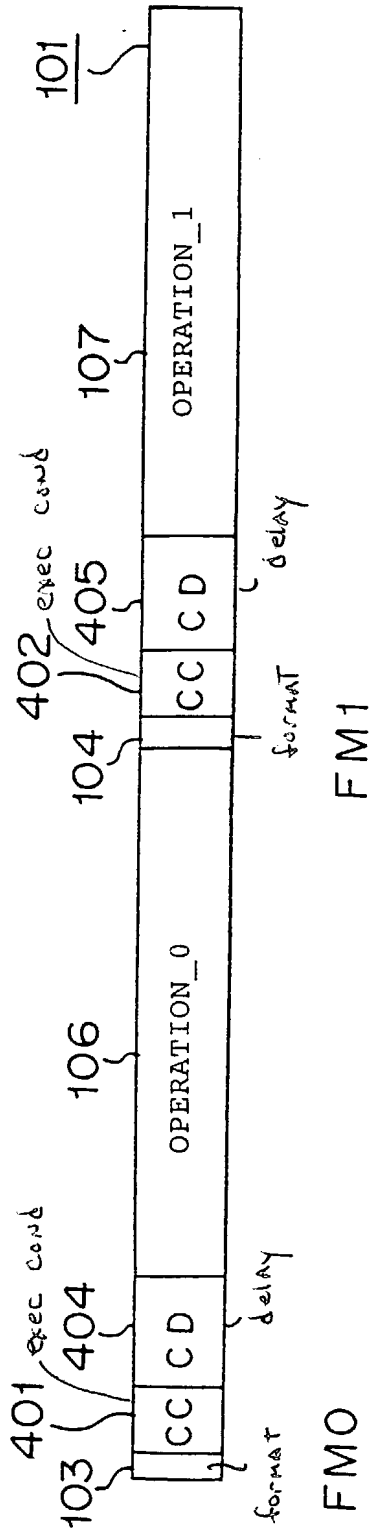


FIGURE 2 (b)

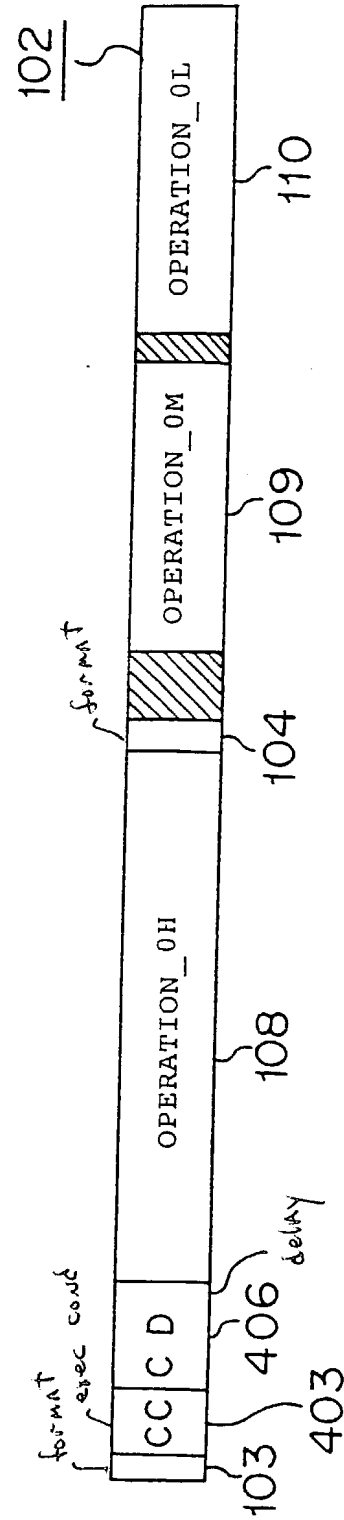
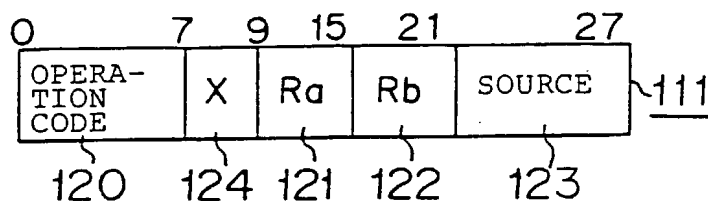
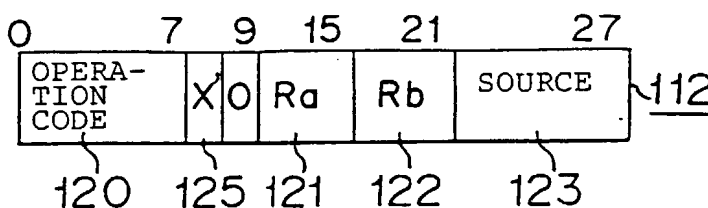


FIGURE 4



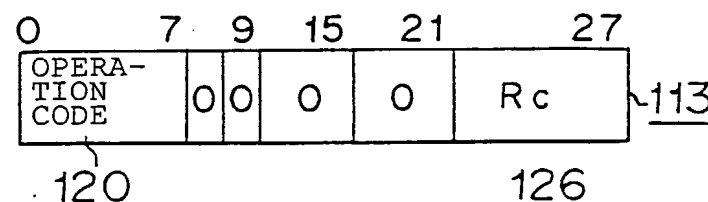
(123)

X=00=>SOURCE=Rc
 X=01=>SOURCE=Rc; Rb++
 X=11=>SOURCE=Rc; Rb--
 X=1-=>SOURCE=IMMEDIATE
 VALUE:6

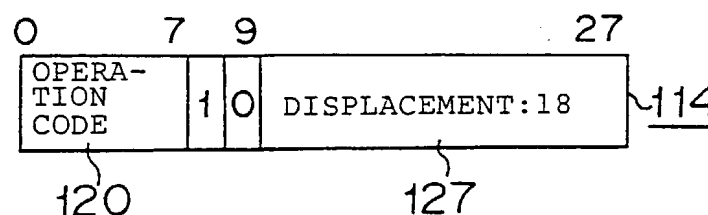


(123)

X'=0=>SOURCE=Rc
 X'=1=>SOURCE=IMMEDIATE
 VALUE:6



Ra, Rb, Rc = Register No.



(129)

Y=0=>SOURCE=Rc
 Y=1=>SOURCE=IMMEDIATE
 VALUE:12
 Z=0=>FOR TESTING ZERO
 Z=1=>FOR TESTING OTHER
 THAN ZERO

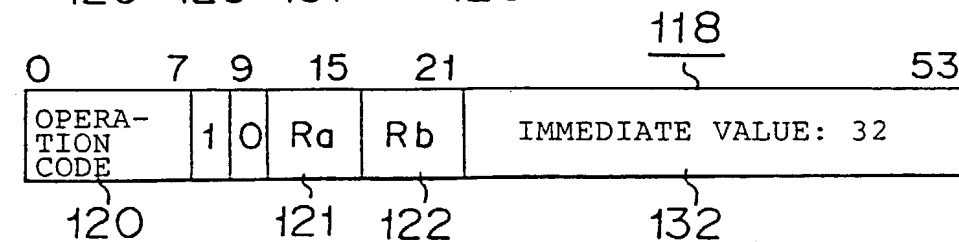
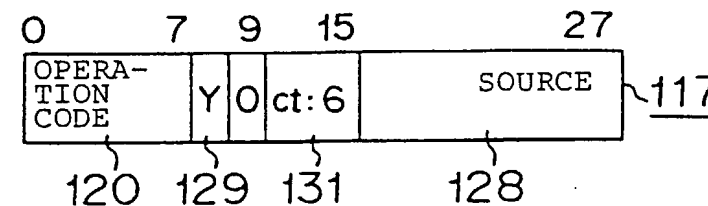
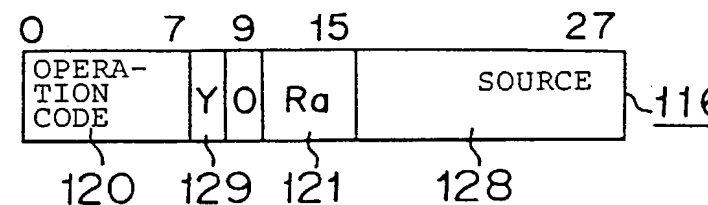
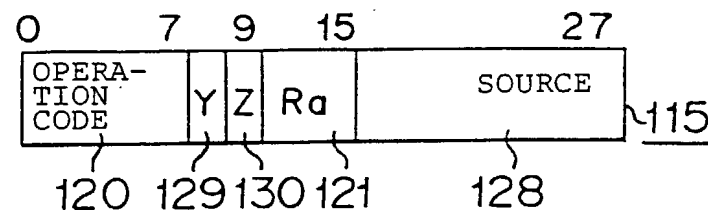


FIGURE 6

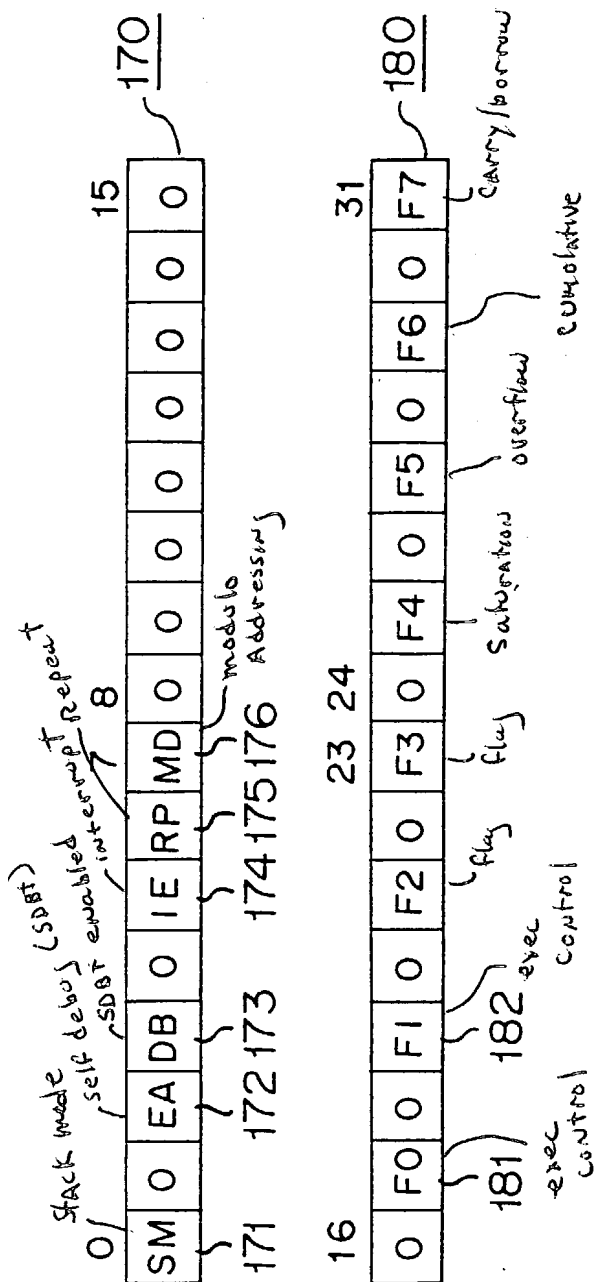
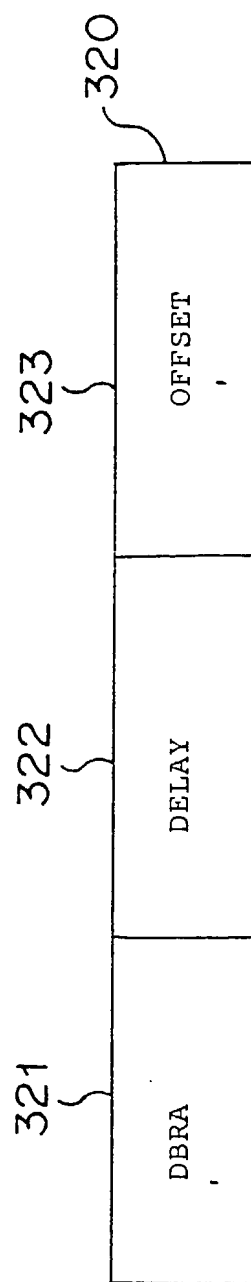


FIGURE 7



BEST AVAILABLE COPY

